

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Dibyapran Sanyal Examiner: MICHAEL D YAARY
Serial No.: 10/827,527 Group Art Unit: 2193
Filed: April 20, 2004 Docket No: 200400476-2
Title: Method and apparatus for generating code for scheduling the execution of binary
code

SUPPLEMENTAL AMENDMENT

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Dear Sir

To supplement the restriction requirement response filed on May 23, 2009, in response to the restriction requirement dated April 24, 2009, please amend the above-identified application prior to examination as follows:

IN THE CLAIMS

Please add the new claims 24-32:

24. (New) The build and compilation process according to claim 21, wherein the dynamic binary translator maps the source code instructions to the target code instructions using the templates.

25. (New) The build and compilation process according to claim 21, wherein the dynamic binary translator outputs the translated target code instruction for processing on a target processor using the templates and fill and analysis routines during compilation of the dynamic binary translator.

26. (New) A dynamic binary translator system for performing a build and compilation process of a dynamic binary translator which operates between any processor or platform types and that translates a source code instruction into a target code instruction, wherein the source